



HOD CSIT <hod_csit@sittechno.org>

Verification of students(four from CSE) exam form is due

2 messages

HOD CSIT <hod_csit@sittechno.org>
To: HOD ECE <hod_ece@sittechno.org>
Cc: guhatamal <guhatamal@yahoo.com>
Bcc: Mithun Roy <mathmithunroy@gmail.com>

Wed, Jul 8, 2020 at 4:00 PM

Respected Sir,

This is to inform you that, the verification process of the bellow mentioned students are incomplete. They have paid their fees on 07/07/2020 and today is the last day of exam fees submission (1200). In this situation, students and their guardians are much worried and create enormous pressure on us. We have somehow misguided on the meeting which is held on 06/07/2020. During that meeting our respected Director Sir said that the University extend one more day for exam form submission. But one the same day the verification process has been closed. As per my knowledge you are also present in that meeting.

As a the principal of B.Tech (119), I would like to request you please provide us your guidance and try to resolve this issue on an urgent basis.

Details of the students (8th Semester CSE final Year) are mentioned below.

1. Abhishek Kumar Yadav -CSE
2. Priyanka Kumari - CSE
3. Pragya Khorla - CSE
4. Arka Prava Chatterjee - CSE

With Regards
Anupam Mukherjee
HOD, CSE
SIT

--
VISION OF CSE DEPARTMENT

“To be a nationwide recognized department that produces versatile computer engineers, capable of adapting to the changing needs of computer and related industry”.

MISSION OF CSE DEPARTMENT

- To impart quality technical education with skills, knowledge and attitude to succeed in Computer Science & Engineering careers.
- To provide knowledge of emerging trends in computer and related industry and foster environment of lifelong learning.
- To develop graduate engineers who investigate research, design and find workable solutions to complex engineering problems with awareness and concern for society and environment.

HOD ECE <hod_ece@sittechno.org>
To: Comptroller of Exam <coe@sittechno.org>
Cc: Kaushik Chakraborty <rainbowkaushik4u@gmail.com>, HOD CSIT <hod_csit@sittechno.org>, "Dr Pradosh Kr. Adhvaryu" <director@sittechno.org>

Wed, Jul 8, 2020 at 4:09 PM

Dear Sir,

Forwarded for your doing the needful.

Regards,
Swapan.

----- Forwarded message -----

From: **HOD CSIT** <hod_csit@sittechno.org>

Date: Wed, Jul 8, 2020 at 4:01 PM

Subject: Verification of students(four from CSE) exam form is due

To: HOD ECE <hod_ece@sittechno.org>

Cc: guhatamal <guhatamal@yahoo.com>

Respected Sir,

This is to inform you that, the verification process of the bellow mentioned students are incomplete. They have paid their fees on 07/07/2020 and today is the last day of exam fees submission (1200). In this situation, students and their guardians are much worried and create enormous pressure on us. We have somehow misguided on the meeting which is held on 06/07/2020. During that meeting our respected Director Sir said that the University extend one more day for exam form submission. But one the same day the verification process has been closed. As per my knowledge you are also present in that meeting.

As a the principal of B.Tech (119), I would like to request you please provide us your guidance and try to resolve this issue on an urgent basis.

Details of the students (8th Semester CSE final Year) are mentioned below.

1. Abhishek Kumar Yadav -CSE
2. Priyanka Kumari - CSE
3. Pragya Khorla - CSE
4. Arka Prava Chatterjee - CSE

With Regards

Anupam Mukherjee

HOD, CSE

SIT

--

VISION OF CSE DEPARTMENT

“To be a nationwide recognized department that produces versatile computer engineers, capable of adapting to the changing needs of computer and related industry”.

MISSION OF CSE DEPARTMENT

- To impart quality technical education with skills, knowledge and attitude to succeed in Computer Science & Engineering careers.
- To provide knowledge of emerging trends in computer and related industry and foster environment of lifelong learning.
- To develop graduate engineers who investigate research, design and find workable solutions to complex engineering problems with awareness and concern for society and environment.



HOD CSIT <hod_csit@sittechno.org>

The enrollment Pending status report from CSE & IT

1 message

HOD CSIT <hod_csit@sittechno.org>

Sat, May 8, 2021 at 12:11 PM

To: Controller of Exam <coe@sittechno.org>

Cc: Debaditya Kundu <debaditya.kundu@gmail.com>, Anupam Mukherjee <anupamsit@gmail.com>, Mithun Roy <mathmithunroy@gmail.com>

Dear Sir,

The enrollment Pending status report from the departments of CSE & IT is attached herewith.

With Regards

HOD, CSE/IT

SIT

--

VISION OF CSE DEPARTMENT

“To be a nationwide recognized department that produces versatile computer engineers, capable of adapting to the changing needs of computer and related industry”.

MISSION OF CSE DEPARTMENT

- To impart quality technical education with skills, knowledge and attitude to succeed in Computer Science & Engineering careers.
- To provide knowledge of emerging trends in computer and related industry and foster environment of lifelong learning.
- To develop graduate engineers who investigate research, design and find workable solutions to complex engineering problems with awareness and concern for society and environment.

**ENROLLMENT PENDING OF CSE-IT.pdf**

860K



HOD CSIT <hod_csit@sittechno.org>

Mismatch words in name title

2 messages

Linkan Majumder <linkan469@gmail.com>

Sun, May 30, 2021 at 1:53 PM

To: "hod_csit@sittechno.org" <hod_csit@sittechno.org>

Dear sir,

I had applied for a name correction in the 1st year as there was a wrong spelling of my name in the college registration number and admit card, but when the application been issued there was an another mismatch of words in the title(as Majum'edr', where the actual is Majum'der') of my name, ever since then I have been trying to make it correct by raising issue to my respective mentors, But the issue not yet been solved and i think it's become really hard yet more to look into this because of the current pandemic situation.

So i am requesting you to look into this matter as soon as possible.

Sincere regards,
Linkan Majumder
Cse 8th semester
Roll 11900117052

HOD CSIT <hod_csit@sittechno.org>

Sun, May 30, 2021 at 7:31 PM

To: Controller of Exam <coe@sittechno.org>

----- Forwarded message -----

From: **Linkan Majumder** <linkan469@gmail.com>

Date: Sun, May 30, 2021 at 1:54 PM

Subject: Mismatch words in name title

To: hod_csit@sittechno.org <hod_csit@sittechno.org>

Dear sir,

I had applied for a name correction in the 1st year as there was a wrong spelling of my name in the college registration number and admit card, but when the application been issued there was an another mismatch of words in the title(as Majum'edr', where the actual is Majum'der') of my name, ever since then I have been trying to make it correct by raising issue to my respective mentors, But the issue not yet been solved and i think it's become really hard yet more to look into this because of the current pandemic situation.

So i am requesting you to look into this matter as soon as possible.

Sincere regards,
Linkan Majumder
Cse 8th semester
Roll 11900117052

--

VISION OF CSE DEPARTMENT

“To be a nationwide recognized department that produces versatile computer engineers, capable of adapting to the changing needs of computer and related industry”.

MISSION OF CSE DEPARTMENT

- To impart quality technical education with skills, knowledge and attitude to succeed in Computer Science &

Engineering careers.

- To provide knowledge of emerging trends in computer and related industry and foster environment of lifelong learning.
- To develop graduate engineers who investigate research, design and find workable solutions to complex engineering problems with awareness and concern for society and environment.



HOD EE <hod_ee@sittechno.org>

Regarding the detailed syllabus of 6th, 7th and 8th Sem of EE dept.

2 messages

HOD EE <hod_ee@sittechno.org>

Wed, Mar 10, 2021 at 1:03 PM

To: feedback.bos.makaut@gmail.com

Cc: "Dr Pradosh Kr. Adhvaryu" <director@sittechno.org>, SIT 119 <info.sittechno119@gmail.com>, ACADEMIC & DISCIPLINARY COMMITTEE <adc.eesit@gmail.com>

Dear Sir,

This is to inform you that the detailed syllabus of 6th, 7th and 8th Semester of Electrical Engineering Department is not available till date. As such we are unable to plan for these semesters.

Kindly look into the matter and advise us.

Thanking you in advance and looking forward for an earliest response from your end.

Shrabani Pal

Head of the Department

Department of Electrical Engineering

Siliguri Institute of Technology

[Techno India Group](#)[Siliguri, Dist. Darjeeling, West Bengal](#)

Ph. : (0353) 277 8002 (O), Fax : (0353)277 8003

Website : www.sittechno.org , www.technoindiagroup.com**[Find us on FACEBOOK](#)****[Official Department Blog](#)****VISION OF THE DEPARTMENT:**

To emerge as a leading Department of Electrical Engineering that caters to the latest needs of power sector, electrical & allied industry in the region.

MISSION OF THE DEPARTMENT:

To evolve as an innovative & globally competent Electrical Engineering department that contributes to the socio-economic growth of region by utilizing the advancement in Electrical Engineering by providing conducive learning and interactive environment to students and faculty.

MAKAUT BoS <feedback.bos.makaut@gmail.com>

Mon, Mar 22, 2021 at 5:05 PM

To: HOD EE <hod_ee@sittechno.org>

Syllabus will be uploaded as soon as possible.

[Quoted text hidden]

--

On behalf of MAKAUT BoS



HOD EE <hod_ee@sittechno.org>

Unable to tag the teacher with paper

1 message

HOD EE <hod_ee@sittechno.org>

Fri, Jul 3, 2020 at 3:12 PM

To: Controller of Exam <coe@sittechno.org>

Cc: "Mr. Arup Das" <arup00723@gmail.com>, jayanta bhusan basu <jbb.sit@gmail.com>, "Ms. Shrabani Pal" <sp.ee.sit@gmail.com>

Dear Sir,

Please note the following with respect to the subject mentioned above.

We are not being able to tag any teacher with the paper named **Electrical System Lab II (Paper code: EE 882)**.

You are requested to look into the matter so that the problem is solved at the earliest.

Thanking you in advance.

Mitul Ranjan Chakraborty

Head of the Department

Department of Electrical Engineering

Siliguri Institute of Technology

[Techno India Group](#)[Siliguri, Dist. Darjeeling, West Bengal](#)

Ph. : (0353) 277 8002 (O), Fax : (0353)277 8003

Website : www.sittechno.org , www.technoindiagroup.com**Find us on FACEBOOK****Official Department Blog****VISION OF THE DEPARTMENT:**

To emerge as a leading Department of Electrical Engineering that caters to the latest needs of power sector, electrical & allied industry in the region.

MISSION OF THE DEPARTMENT:

To evolve as an innovative & globally competent Electrical Engineering department that contributes to the socio-economic growth of region by utilizing the advancement in Electrical Engineering by providing conducive learning and interactive environment to students and faculty.



HOD EE <hod_ee@sittechno.org>

Teacher paper tagging

1 message

HOD EE <hod_ee@sittechno.org>

Wed, Feb 5, 2020 at 3:22 PM

To: Comptroller of Exam <coe@sittechno.org>

Bcc: "Ms.Shrabani Pal" <sp.ee.sit@gmail.com>, jayanta bhusan basu <jbb.sit@gmail.com>

Dear Sir,

With reference to the verbal communication with you, please note that we have tried to follow your suggestion to solve the teacher paper tagging for the Paper "**Values & Ethics in Profession**" - both old (HU 401) & new syllabus(HM EE 401). But the problem is still there. We are unable to tag the concerned faculty to the paper mentioned.

You are therefore requested to look into the matter to solve the issue.

Regards.

Mitul Ranjan Chakraborty

Head of the Department

Department of Electrical Engineering

Siliguri Institute of Technology

[Techno India Group](#)[Siliguri, Dist. Darjeeling, West Bengal](#)

Ph : (0353) 277 8002 (O), Fax : (0353)277 8003

Website : www.sittechno.org , www.technoindiagroup.com**[Find us on FACEBOOK](#)****[Official Department Blog](#)****VISION OF THE DEPARTMENT:**

To emerge as a leading Department of Electrical Engineering that caters to the latest needs of power sector, electrical & allied industry in the region.

MISSION OF THE DEPARTMENT:

To evolve as an innovative & globally competent Electrical Engineering department that contributes to the socio - economic growth of region by utilizing the advancement in Electrical Engineering by providing conducive learning and interactive environment to students and faculty.



HOD CSIT <hod_csit@sittechno.org>

Discrepancy regarding backlog paper.

2 messages

Adarsh Rai <adarshrai181@gmail.com>
To: hod_csit@sittechno.org

Thu, Apr 22, 2021 at 9:10 PM

Respected Sir,

I am Adarsh Rai student of Siliguri Institute of Technology currently studying in 8th semester. Sir i had a backlog in mathematics paper in my 4th semester. I filled up the backlog form in order to reappear for my back paper. The examination was supposed to be conducted in the six semester but there was no notification or any kind of information regarding the same. Since i am in 8th semester and this is the last even semester, sir i wanted to know what would be done regarding this matter. Sir there are many students like me wanting to know some information regarding the same.

Yours Sincerely,
Adarsh Rai.

HOD CSIT <hod_csit@sittechno.org>
To: Controller of Exam <coe@sittechno.org>

Fri, Apr 23, 2021 at 12:12 PM

----- Forwarded message -----

From: **Adarsh Rai** <adarshrai181@gmail.com>
Date: Thu, 22 Apr 2021, 9:10 pm
Subject: Discrepancy regarding backlog paper.
To: <hod_csit@sittechno.org>

Respected Sir,

I am Adarsh Rai student of Siliguri Institute of Technology currently studying in 8th semester. Sir i had a backlog in mathematics paper in my 4th semester. I filled up the backlog form in order to reappear for my back paper. The examination was supposed to be conducted in the six semester but there was no notification or any kind of information regarding the same. Since i am in 8th semester and this is the last even semester, sir i wanted to know what would be done regarding this matter. Sir there are many students like me wanting to know some information regarding the same.

Yours Sincerely,
Adarsh Rai.



HOD CSIT <hod_csit@sittechno.org>

The enrollment Pending status report from CSE & IT

1 message

HOD CSIT <hod_csit@sittechno.org>

Sat, May 8, 2021 at 12:11 PM

To: Controller of Exam <coe@sittechno.org>

Cc: Debaditya Kundu <debaditya.kundu@gmail.com>, Anupam Mukherjee <anupamsit@gmail.com>, Mithun Roy <mathmithunroy@gmail.com>

Dear Sir,

The enrollment Pending status report from the departments of CSE & IT is attached herewith.

With Regards

HOD, CSE/IT

SIT

--

VISION OF CSE DEPARTMENT

“To be a nationwide recognized department that produces versatile computer engineers, capable of adapting to the changing needs of computer and related industry”.

MISSION OF CSE DEPARTMENT

- To impart quality technical education with skills, knowledge and attitude to succeed in Computer Science & Engineering careers.
- To provide knowledge of emerging trends in computer and related industry and foster environment of lifelong learning.
- To develop graduate engineers who investigate research, design and find workable solutions to complex engineering problems with awareness and concern for society and environment.

**ENROLLMENT PENDING OF CSE-IT.pdf**

860K